

6182184

FIG 1

005402012 0222000

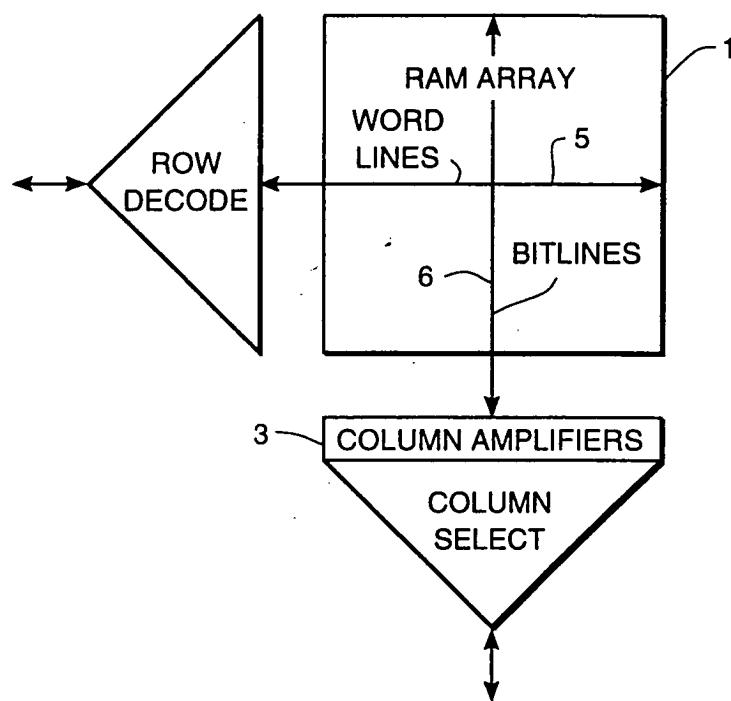


FIG - 2

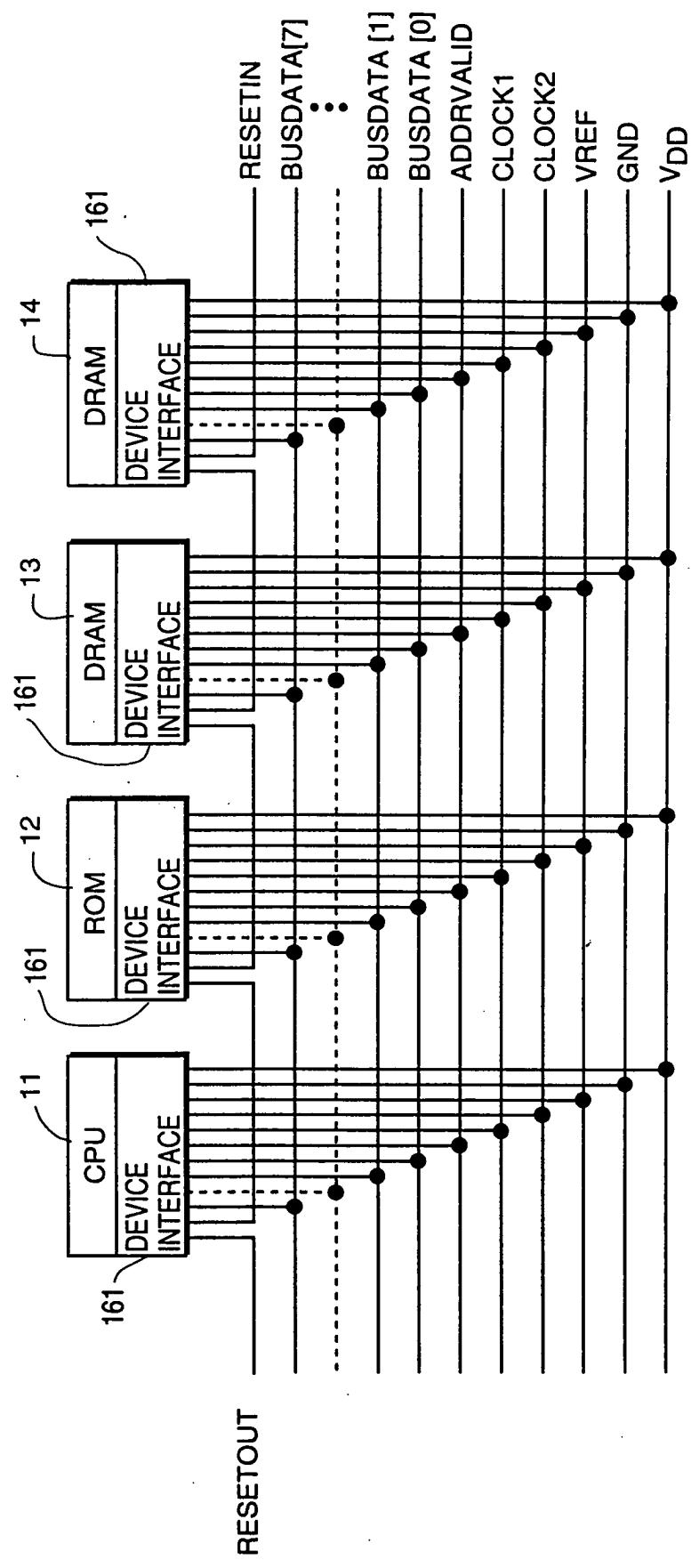
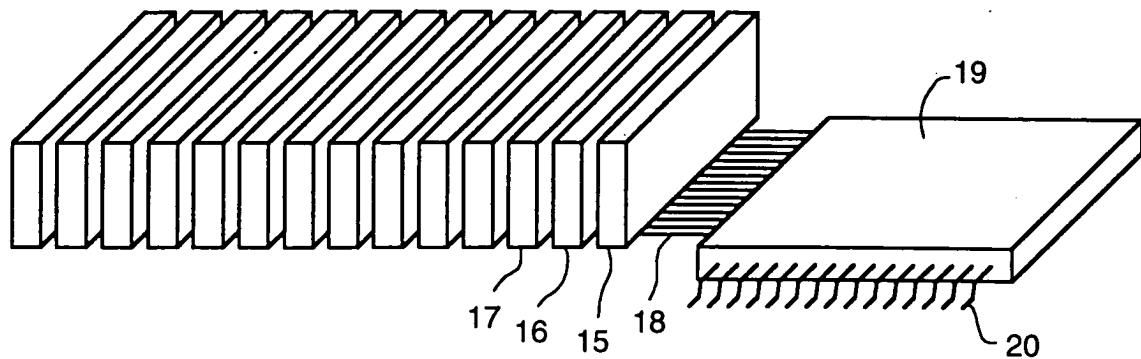


FIG 3



REGULAR ACCESS

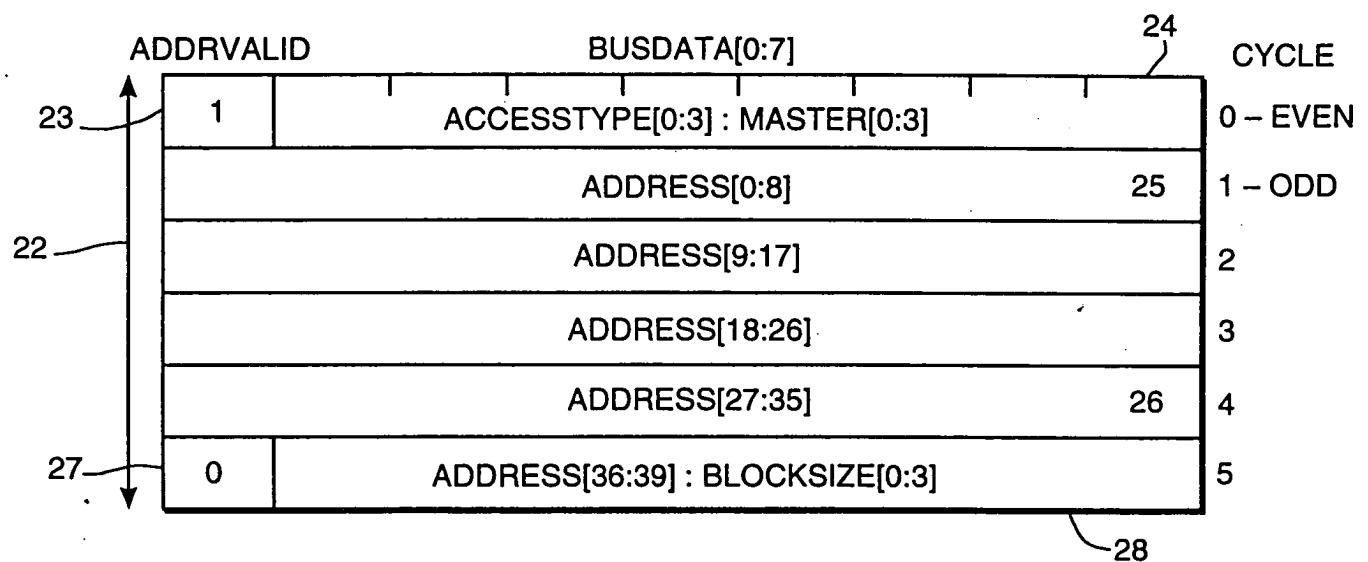
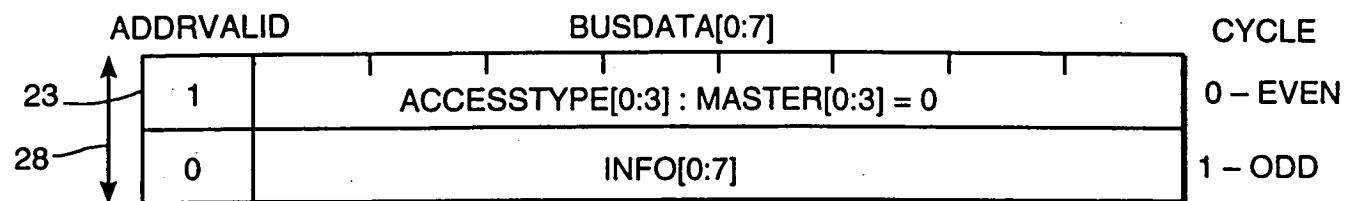
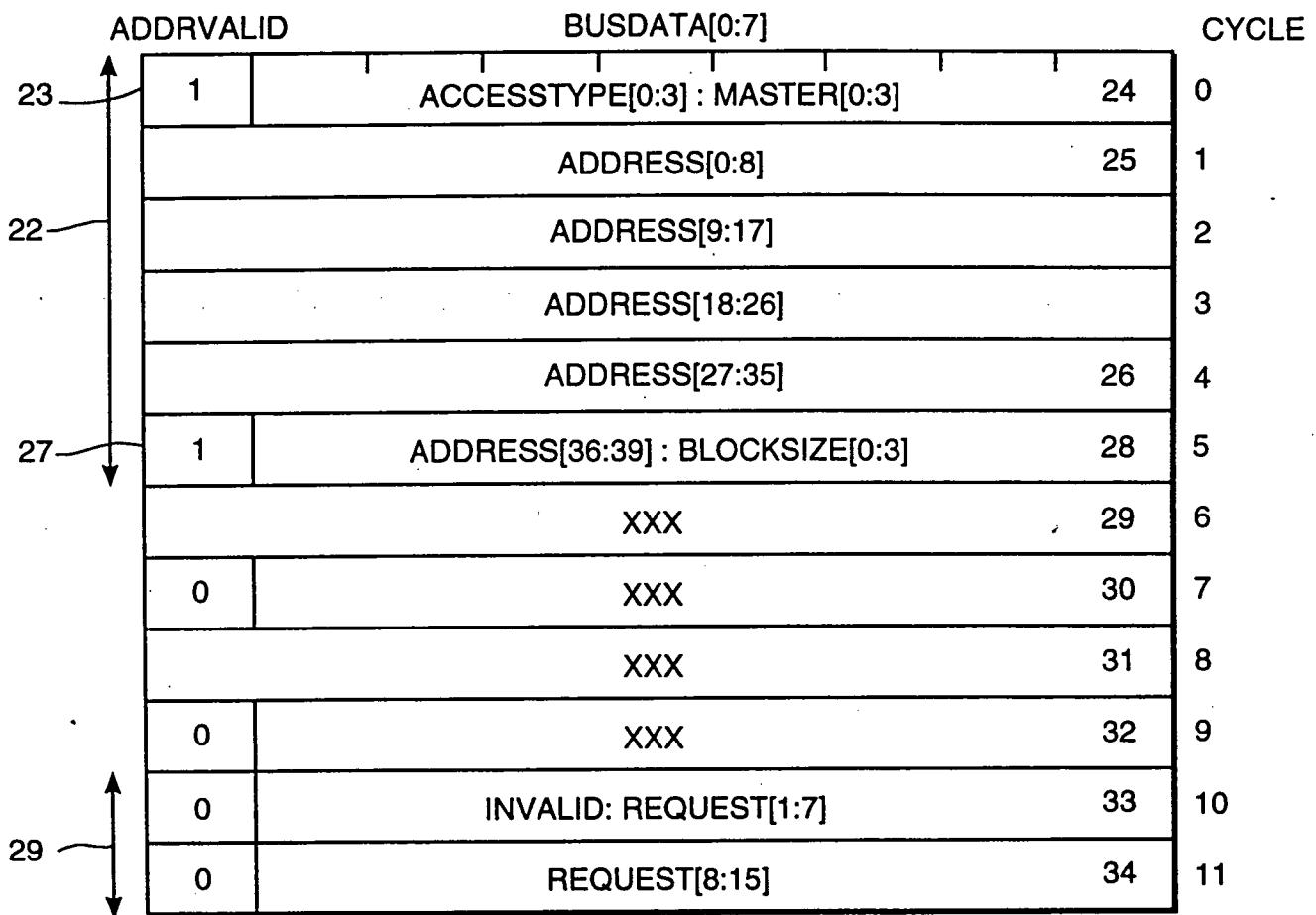


FIG 4

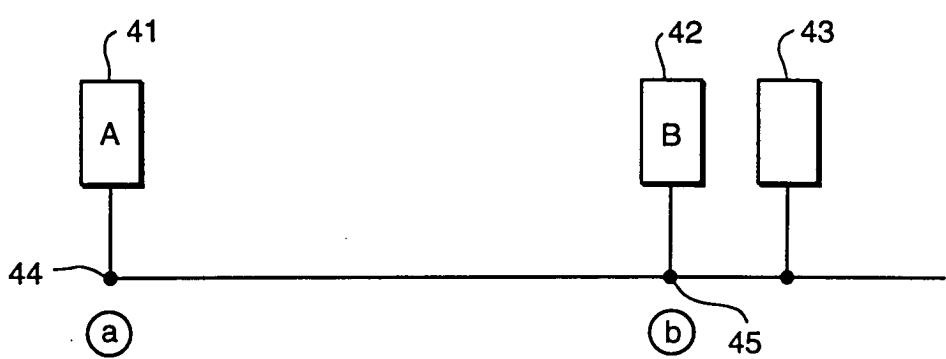
## REJECT (NACK) CONTROL PACKET



## FIG 5

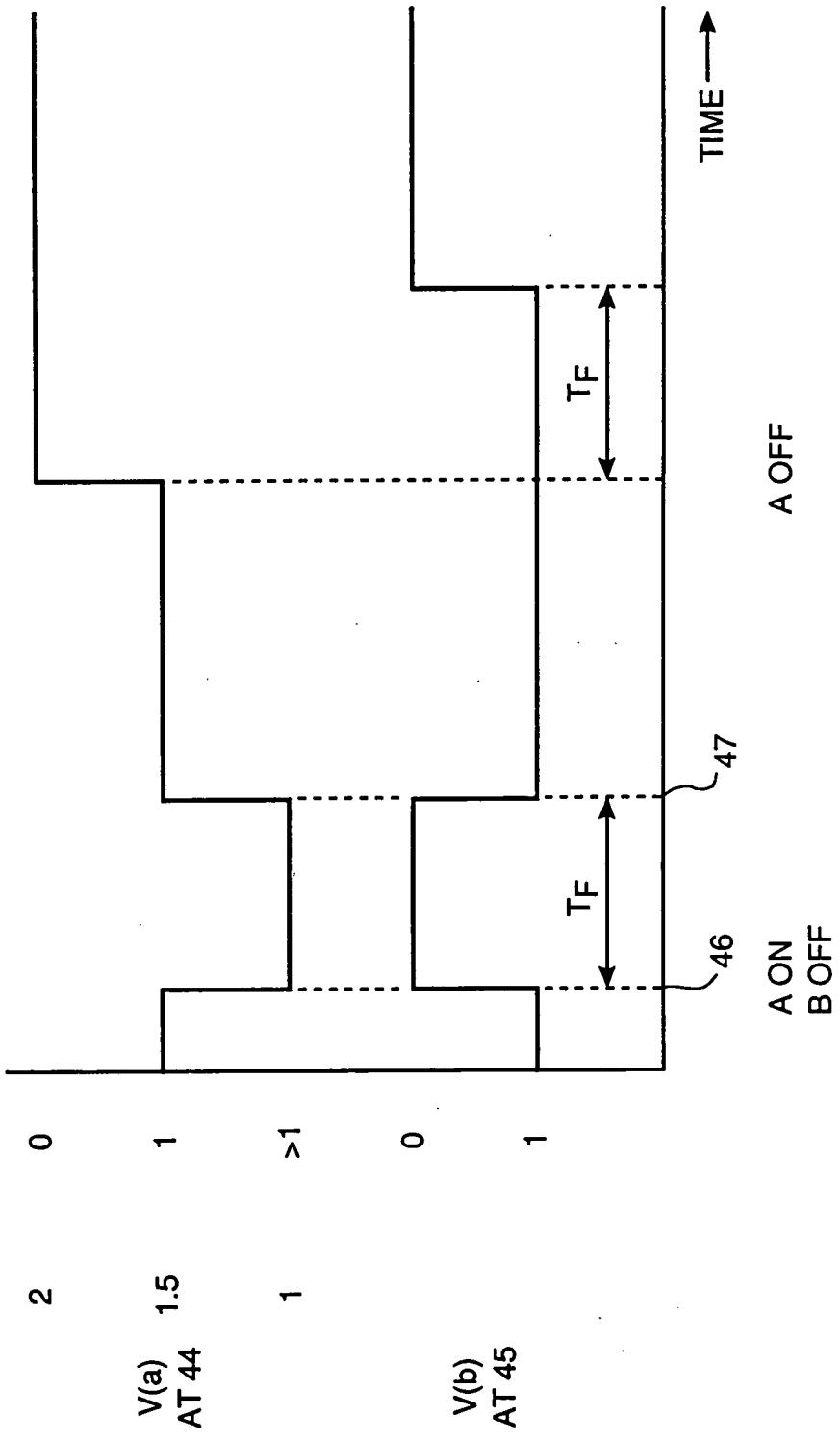


## FIG 6

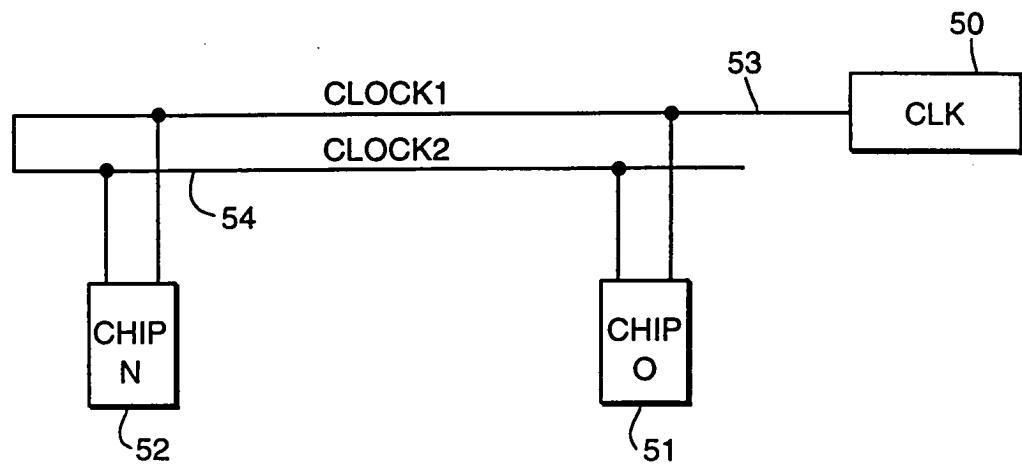


**FIG\_7A**

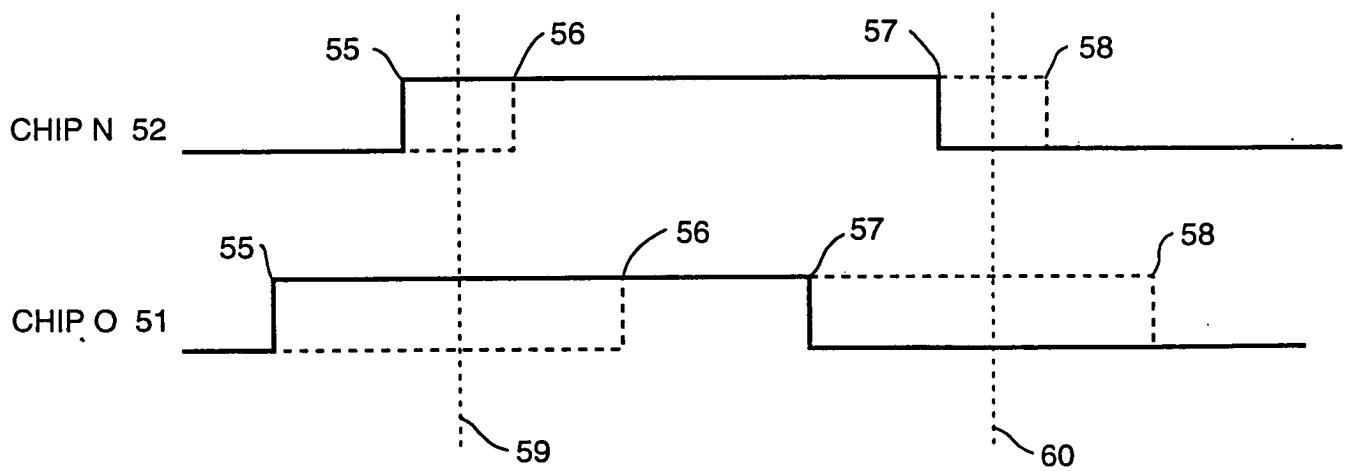
VOLTAGE LOGICAL  
VALUE



TIME →  
44 45 46 47 48  
A ON  
B OFF

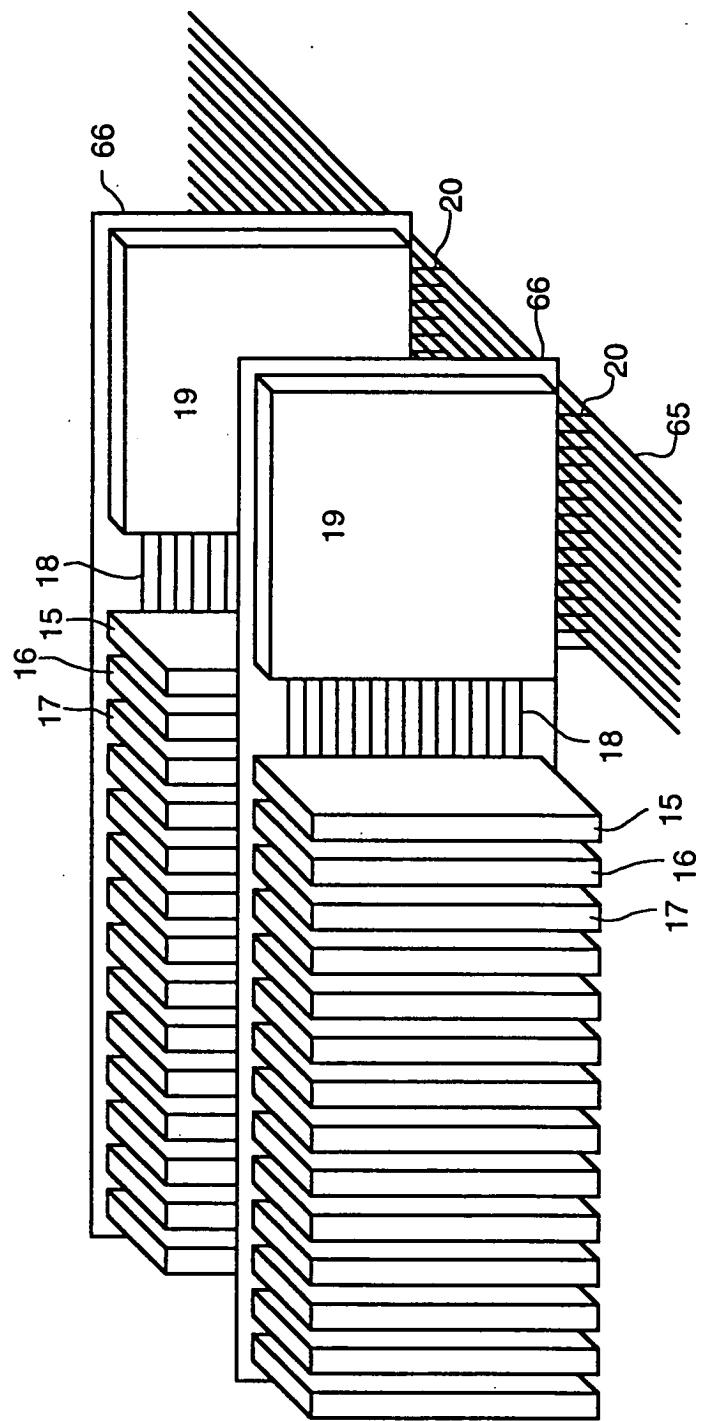


**FIG\_XA**



**FIG\_XB**

FIG. 5



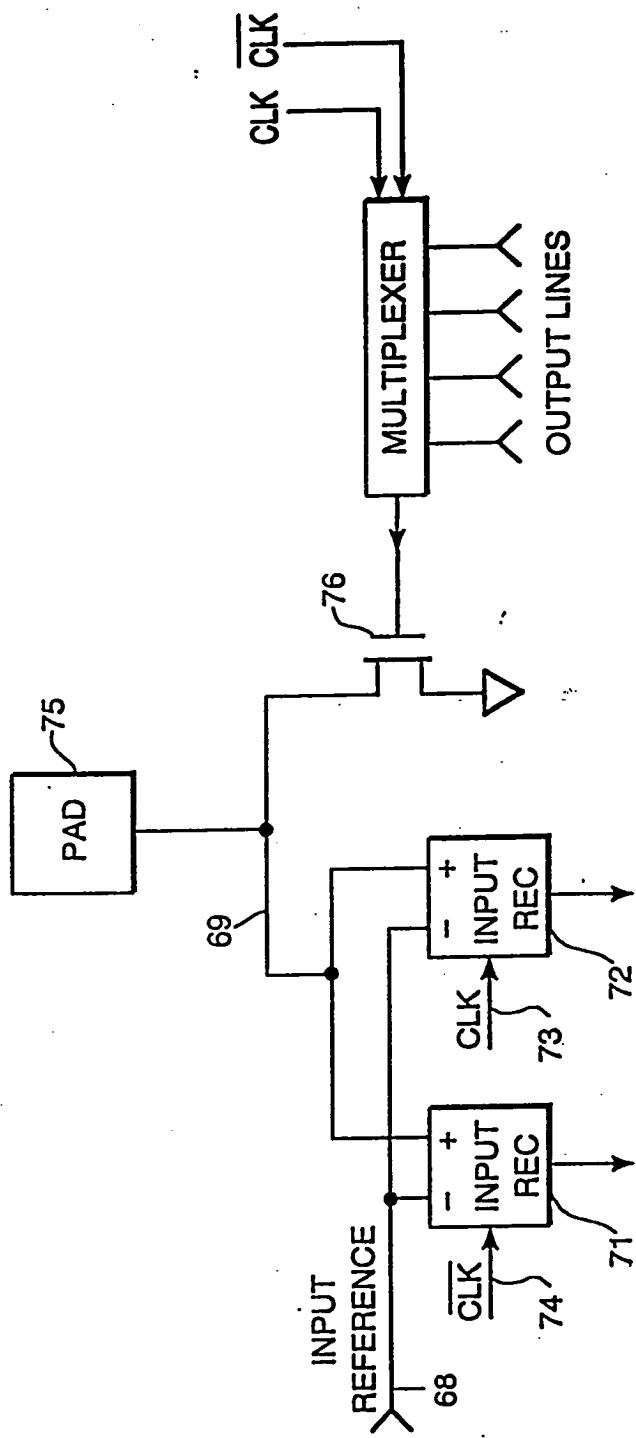


FIG - 10

FIG. 11

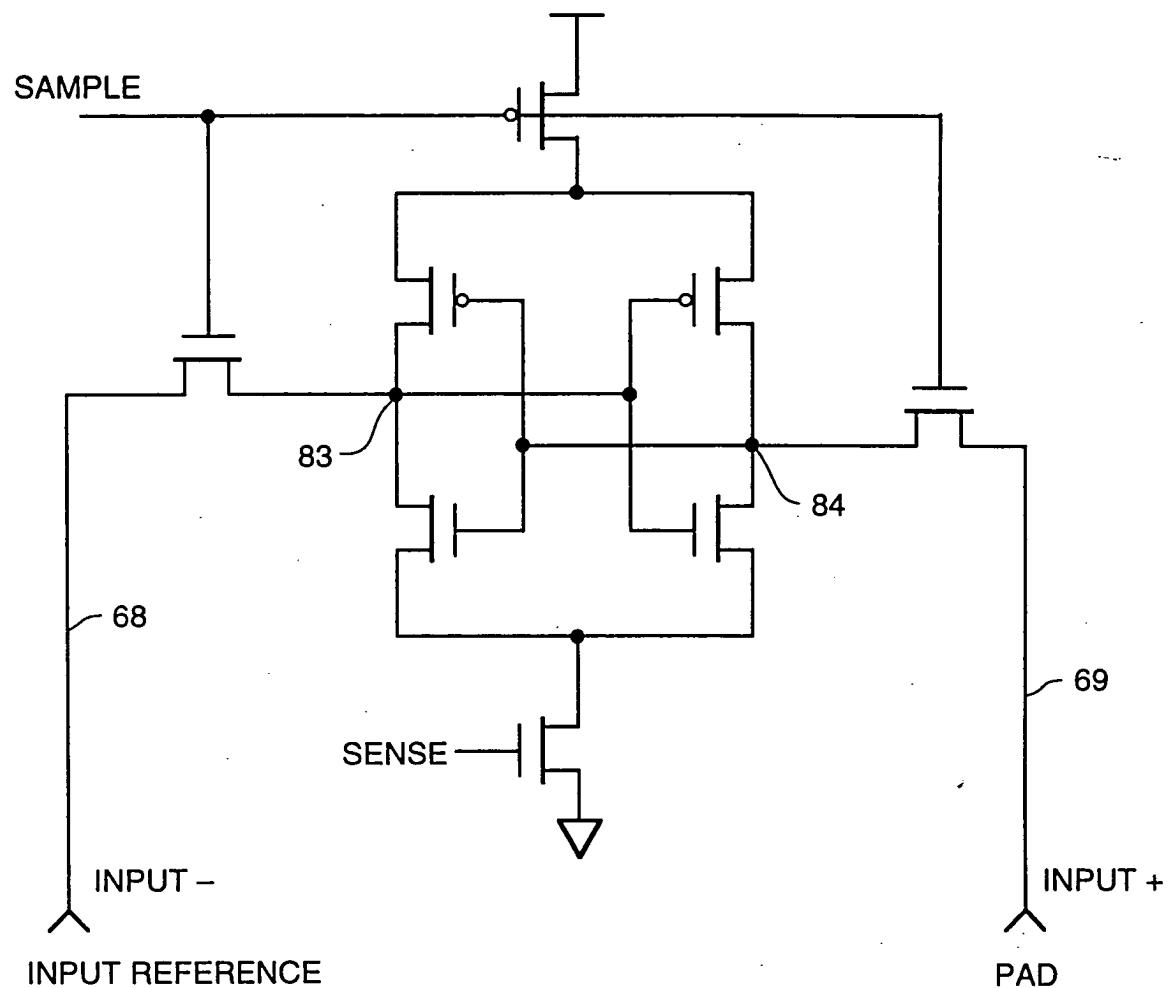
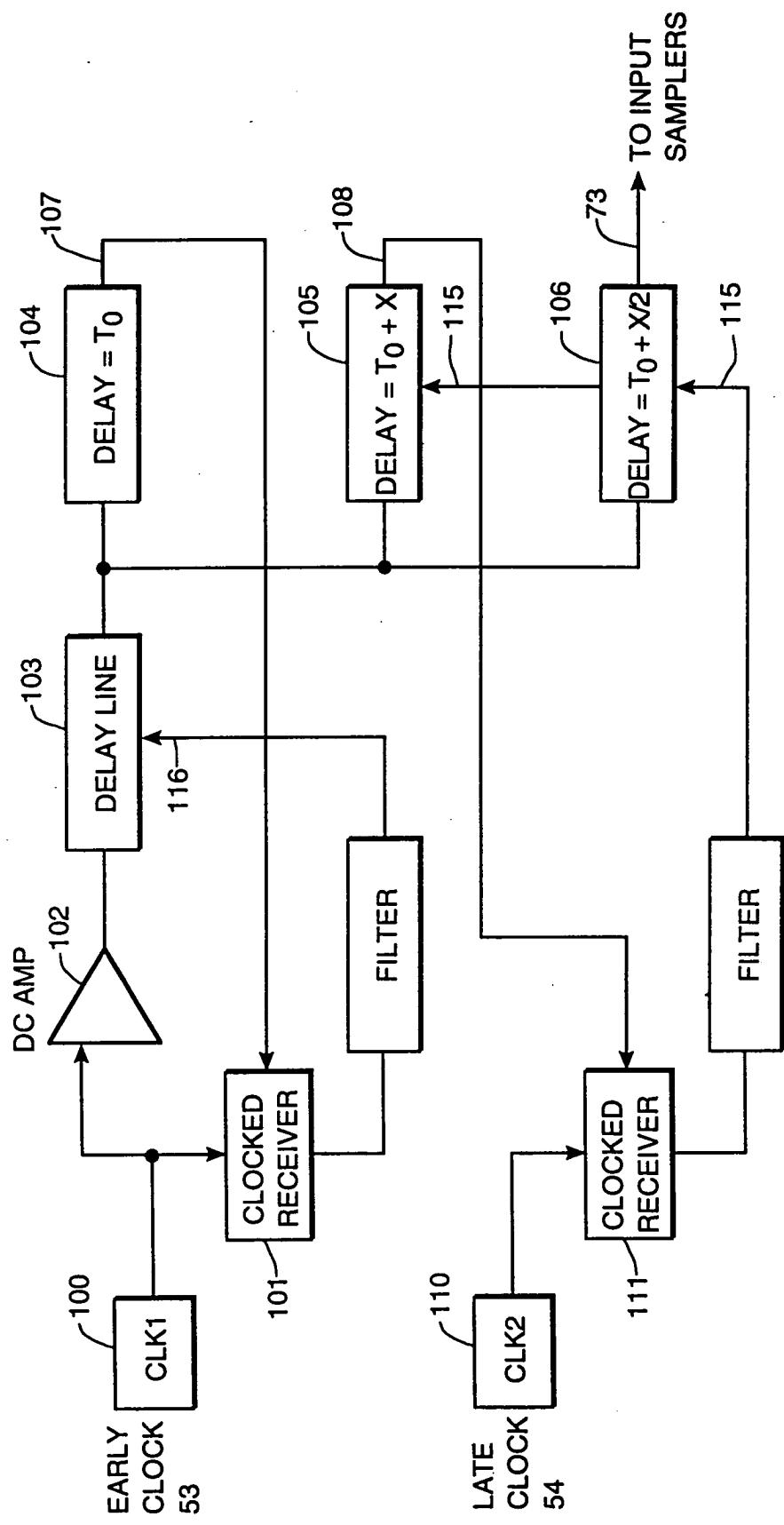
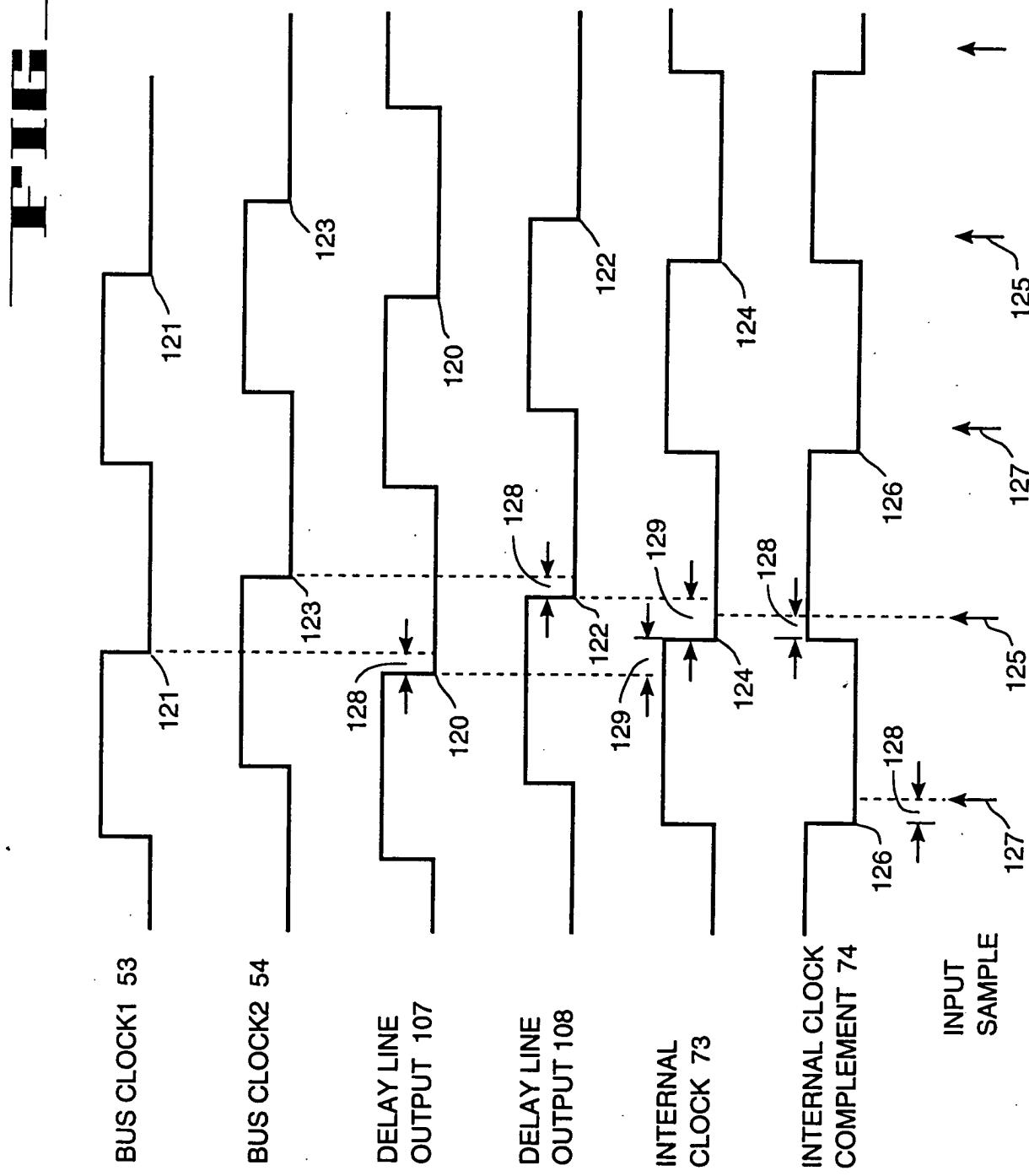


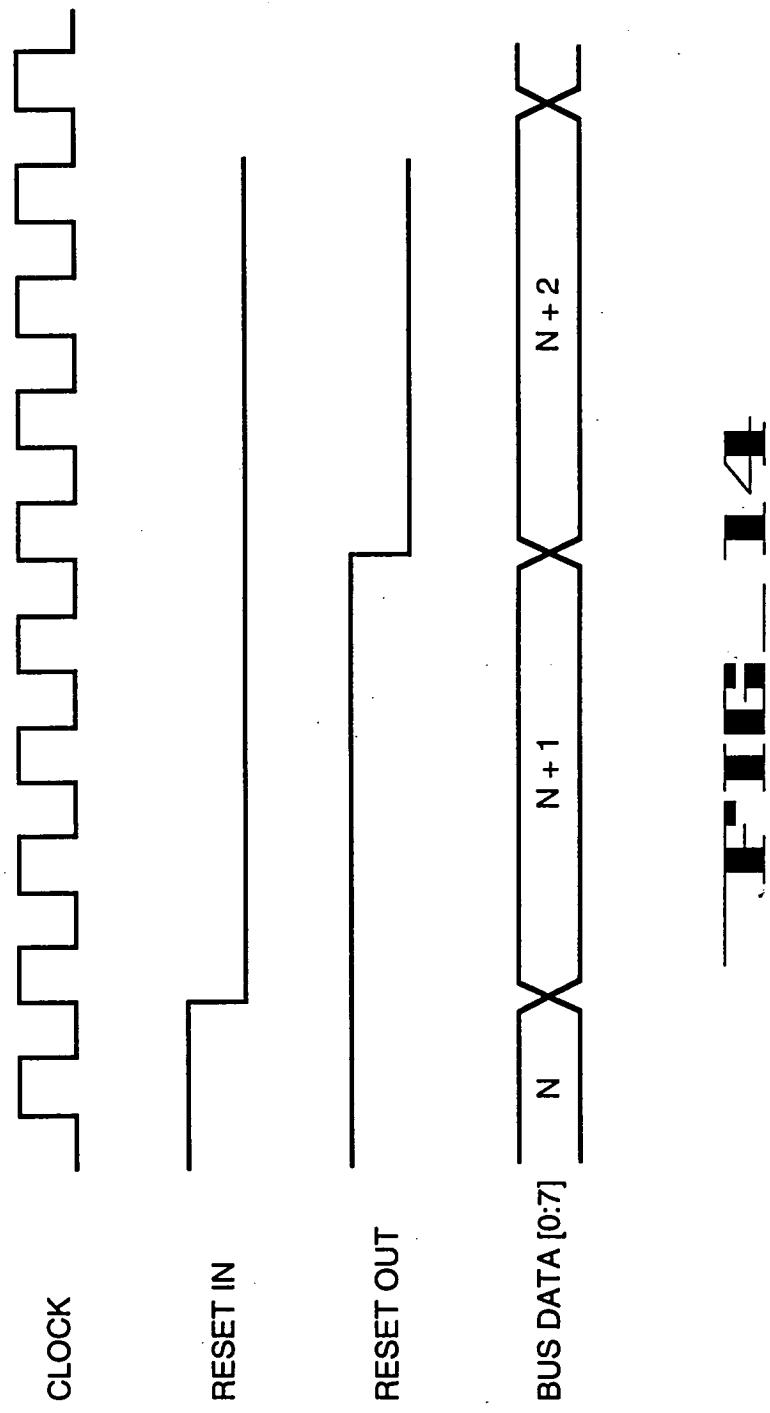
FIG. 12



MÉTIER



00000000000000000000000000000000



FIG\_15

